REMARKS

Claims 1, 3-8, and 10-22 remain in the application for consideration of the Examiner with Claim 9 standing canceled.

The Examiner alleges that Claims 1 and 3-6 of this application conflicts with Claims 1-5 of application 10/005,463. Additionally, the Examiner requires the Applicant to either cancel the conflicting claims from one application or maintain a clear line of demarcation between the applications.

The conflict is not seen by Applicant. Clarification is requested.

Claims 1 and 3-6 were rejected under 35 U.S.C. §101 as claiming the same invention of Claims 1-16 of 10/005,463.

Applicants respectfully submit that this rejection is premature since there has been no application in fact patented or allowed. Consequently, Applicants respectfully request this rejection to be held in abeyance until one application has in fact been patented.

Turning now to the art rejection, Claims 1, 3-9, and 20-22 were rejected under 35 U.S.C. §102(b) as being anticipated by Uscategui; and Claim 6 was rejected under 35 U.S.C. §103 as being unpatentable over Uscategui in view of alleged admitted prior art (AAPA).

These rejections are respectively traversed.

It is respectfully submitted that Uscategui does not disclose or suggest the presently claimed invention including the pre-driver sub-stage being adapted to accept a current signal (δI_{in}) from the input transconductance g_m cell as defined in independent Claim 1.

The Examiner alleges that element g_m is inherent to small signal model of amplifier circuit referring to El ments Q1-Q4.

Notwithstanding the allegations of the Examiner, Uscategui discloses Q1-Q4 as input stage 12 and there is no indication that a transconductance cell is disclosed.

Furthermore, Uscategui does not disclose or suggest he presently claimed invention including the step of employing a plurality of non-translinear loops in the pre-driver sub-stage circuits and final stage in the various forms in independent Claims 20 and 22.

Uscategui does not disclose the translinear loops in the final stage.

Whether or not AAPA discloses a DSL driver and whether or not one of ordinary skill in the art would consider modifying Uscategui is of no moment since the resulting construction would in no way disclose or suggest the presently claimed invention.

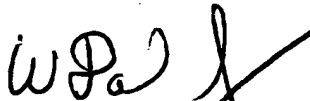Applicants appreciate the indication that Claims 10-19 are allowed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including

extension of tim   fees, to the d posit account of Texas Instruments Incorporated,

Account No. 20-0668.

Respectfully submitted,

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